

# Optimization of Deterministic Timed Weighted Marked Graphs

Zhou He, Zhiwu Li, and Alessandro Giua

## Abstract

Timed marked graphs, a special class of Petri nets, are extensively used to model and analyze cyclic manufacturing systems. Weighted marked graphs are convenient to model systems with bulk services and arrivals. We consider two problems of practical importance for this class of nets. The marking optimization problem consists in finding an initial marking to minimize the weighted sum of tokens in places while the average cycle time is less than or equal to a given value. The cycle time optimization problem consists in finding an initial marking to minimize the average cycle time while the weighted sum of tokens in places is less than or equal to a given value. We propose two heuristic algorithms to solve these problems. Several simulation studies show that the effectiveness of the proposed approach is significantly faster than existing ones.

*Note to Practitioners*—This paper is motivated by the optimization of manufacturing systems such as assembly lines and job-shops. The quantity of products which have to be stored or moved and the number and type of machines which operate the system have economical consequences. Therefore, the main problem for designers is to find a trade-off between minimizing the cost of the resources and maximizing the system's throughput. To this end, we formalize and address two problems. First, we aim to minimize the use of resources required to meet a desired throughput. Second, we aim to maximize the throughput with a given set of available resources. However, the existing results fail to provide practically effective and computationally efficient methods to analyze and solve the problems in such systems. The proposed iterative heuristic algorithms can find a proper schedule for a system, which is significant and useful to manufacturing engineers.

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## I. INTRODUCTION

Timed Petri nets are a well known tool to model discrete event systems and represent their dynamic behavior. In this paper, we study a particular class of timed Petri nets called *timed weighted marked graphs* (TWMGs). The main structural feature of this class of nets is that each place has only one input and one output transition. Moreover, the firing delay of each transition is *deterministic*.

Timed weighted marked graphs and timed marked graphs (TMGs) find wide applications in manufacturing. They can model complex assembly lines and solve cyclic scheduling problems. Workshop operations and products are usually modeled by transitions and tokens, respectively. Between two successive transformations, semi-finished products have to be stored or moved from a workshop to another. The amount of products, also called Work In Process (WIP), that have to be stored or moved may have economical consequence. Therefore, the main problem for designers is to determine a proper schedule of WIP that allows the system to reach prespecified productivity while the amount of WIP is the smallest.

Some results have been developed for this class of Petri nets. Teruel *et al.* [1] proposed several techniques for the analysis of WMGs. Nakamura and Silva [2] and Campos *et al.* [3] developed methods to compute the average cycle time of TMGs for a given initial marking. Benabid-Najjar *et al.* [4] discussed the periodic schedules of bounded TWMGs and presented polynomial algorithms to check the existence of periodic schedules. Giua *et al.* [5] dealt with the problem of allocating a given number of tokens in a TMG so as to maximize the firing rate (i.e., the inverse of the cycle time) and proposed three different procedures to solve the problem. Laftit *et al.* [6] investigated the problem of reaching on TMGs a desired average cycle time that is less than a given value, while minimizing an invariant linear criterion. In their work, a heuristic algorithm and an exact algorithm are presented. However, in the literature, few studies are found to consider the marking optimization problem of TWMGs. Sauer [7] addressed the problem of finding an initial marking to minimize the weighted sum of tokens in places while the average cycle time is less than or equal to a given value, and proposed a heuristic solution based on an iterative process. Touris and Sauer [8] presented an approach based on the branch and bound to solve the same problem.

This paper copes with the marking optimization problem of a TWMG and the related cycle time optimization problem. The marking optimization problem consists in finding an initial marking to minimize the weighted sum of tokens in places while the average cycle time is less than or equal to a given value. The cycle time optimization problem consists in finding an initial marking to minimize the average cycle time while the weighted sum of tokens in places is less than or equal to a given value.

In the first part, we propose an iterative heuristic algorithm (Algorithm 1) to solve the marking optimization problem. At each step, we select places from some circuits to which we add tokens until the average cycle time is less than or equal to the desired value. Numerical simulation studies presented in Section IV-D show that the proposed method requires less iteration steps and thus is much faster than the approach in [7]. In some special cases the objective function that we obtained may be greater (i.e., worse) than the one found by Sauer. However, we show that by combining the two approaches, i.e., the one in [7] and the one proposed in this work, we can

always reach the same objective function in [7] with a significant reduction of computational costs. Some of these results were presented in a preliminary form in [9].

In the second part of this paper, we study the cycle time optimization problem of a TWMG, an issue that to the best of our knowledge has not been addressed in the literature. Another iterative heuristic algorithm (Algorithm 2) is presented. At each step, we provide a criterion to select one place to which tokens are allocated until all available tokens (i.e., resources) are used up or the average cycle time reaches the lower bound.

The main contributions of the present paper can be summarized as follows:

1) A heuristic solution combined with an analytical method for the marking optimization problem is presented. By several experimental studies, we show that the combined method (He+Sauer) is more efficient than previous one in most cases.

2) The cycle time optimization problem of TWMGs is originally presented. A novel heuristic solution is proposed which is fast and efficient.

This paper is structured as follows. In the following section, we briefly recall some basic concepts and the main properties of TWMGs. Section III presents the problem statement. In Section IV, we propose a heuristic solution for the marking optimization problem based on a live marking. Following the algorithm, some numerical examples are shown to illustrate it and details are given to compare the proposed method with the one in [7]. In Section V, the cycle time optimization problem is considered and another heuristic algorithm is proposed. Some applications in manufacturing are presented at the end of this section. Conclusions are finally drawn in Section VI.

## II. BACKGROUND

### A. Generalities

We assume that the reader is familiar with the structure, firing rules, and basic properties of Petri nets (see [1], [10] and [11]). In this section, we will recall the formalism used in the paper. A *place/transition* net ( $P/T$  net) is a structure  $N = (P, T, Pre, Post)$ , where  $P$  is a set of  $n$  places;  $T$  is a set of  $m$  transitions;  $Pre : P \times T \rightarrow \mathbb{N}$  and  $Post : P \times T \rightarrow \mathbb{N}$  are the *pre-* and *post-incidence* functions, respectively, that specify the arcs; and  $C = Post - Pre$  is the *incidence matrix*. Note that  $\mathbb{N}$  is a set of non-negative integers.

A vector  $\mathbf{x} = [x_1, x_2, \dots, x_m]^T \in \mathbb{N}^{|T|}$  such that  $\mathbf{x} \neq \mathbf{0}$  and  $C \cdot \mathbf{x} = \mathbf{0}$  is a *T-semiflow*. A vector  $\mathbf{y} = [y_1, y_2, \dots, y_n]^T \in \mathbb{N}^{|P|}$  such that  $\mathbf{y} \neq \mathbf{0}$  and  $\mathbf{y}^T \cdot C = \mathbf{0}^T$  is a *P-semiflow*. The *supports* of a T-semiflow and a P-semiflow are defined by  $\|\mathbf{x}\| = \{t_i \in T | x_i > 0\}$  and  $\|\mathbf{y}\| = \{p_i \in P | y_i > 0\}$ , respectively. A *minimal* T-semiflow<sup>1</sup> (P-semiflow) is a T-semiflow  $\|\mathbf{x}\|$  (P-semiflow  $\|\mathbf{y}\|$ ) that is not a superset of the support of any other T-semiflow (P-semiflow), and its components are mutually prime.

A *marking* is a vector  $\mathbf{M} : P \rightarrow \mathbb{N}$  that assigns to each place of a  $P/T$  net a non-negative integer number of tokens; we denote the marking of place  $p$  as  $\mathbf{M}(p)$ . A  $P/T$  system or *net system*  $\langle N, \mathbf{M}_0 \rangle$  is a net  $N$  with an

<sup>1</sup>This is also called a minimal and minimal support semiflow in some references. For the sake of simplicity, we call it a minimal semiflow.

initial marking  $M_0$ . A transition  $t$  is enabled at  $M$  if  $M \geq Pre(\cdot, t)$  and an enabled transition  $t$  may fire yielding a marking  $M'$  with

$$M' = M + C(\cdot, t) \quad (1)$$

where  $Pre(\cdot, t)$  (resp.  $C(\cdot, t)$ ) denotes the column of the matrix  $Pre$  (resp.  $C$ ) associated with transition  $t$ .

A  $P/T$  net is said to be *ordinary* when all arc weights are 1's. A *marked graph* (MG, also called an event graph) is an ordinary Petri net that satisfies the condition  $|\bullet p| = |p \bullet| = 1$ . A *weighted marked graph* (WMG, also called a weighted event graph) is a net that also satisfies this condition but may not be ordinary, i.e., the weight associated with an arc can be a non-negative integer.

A deterministic timed  $P/T$  net is a pair  $N^\delta = (N, \delta)$ , where  $N = (P, T, Pre, Post)$  is a  $P/T$  net, and  $\delta : T \rightarrow \mathbb{N}$ , called *firing delay*, assigns a non-negative integer firing duration to each transition. A transition with a firing delay equal to 0 is said to be immediate. A *clock*  $\theta_i(t)$  associated with transition  $t$  represents the residual time to fire  $t$  at marking  $M_i$ . If a transition  $t$  is not enabled at marking  $M_i$ , it is usually assumed that  $\theta_i(t) = +\infty$ . We denote the *clock vector*  $\theta_i = (\theta_i(t_1), \theta_i(t_2), \dots, \theta_i(t_m))$ .

We denote the *state* of a TWMG as  $[M; \theta]$ . Thus the evolution of a deterministic TWMG will be described by the marking  $M_i$ , the clock vector  $\theta_i$  and the time instant  $\tau_i$  at which marking  $M_i$  is reached (for  $i = 1, 2, \dots, n$ ). We initialize  $\tau_0 = 0$  for the initial marking  $M_0$ .

When a transition  $t$  becomes enabled, it cannot fire before the time  $\delta(t)$  has elapsed. Under the *As Soon As Possible* (ASAP) execution policy, a transition  $t$  will fire *exactly* after  $t$  is enabled for a time  $\delta(t)$ . In this paper, we deal with TWMGs which are conflict-free nets, and thus the firing of a transition  $t$  does not disable any other transitions. In the ASAP execution, from state  $[M_i; \theta_i]$ , all transitions that have a minimal value of the clock can be fired simultaneously after a time  $\phi_i = \min_{t \in T} \theta_i(t) = \tau_{i+1} - \tau_i$  and the TWMG reaches a new state  $[M_{i+1}; \theta_{i+1}]$ . The marking will change as shown in Eq. (1) and the clock will be updated by the following equations:

$$\theta_0(t) = \begin{cases} \delta(t), & \text{if } M_0[t] \\ +\infty, & \text{if } \neg M_0[t] \end{cases} \quad (2)$$

$$\theta_i(t) = \begin{cases} \delta(t), & \text{if } \neg M_{i-1}[t] \text{ and } M_i[t] \\ & \text{or } t \text{ fired at } M_{i-1} \text{ and } M_i[t] \\ +\infty, & \text{if } \neg M_i[t] \\ \theta_{i-1}(t) - \phi_{i-1}, & \text{otherwise} \end{cases} \quad (3)$$

where  $\neg M[t]$  means that  $t$  is not enabled at marking  $M$ . In this paper, we consider a single server semantics, i.e., we assume that each transition can fire only once at each time instant even if its enabling degree is greater than one (see [7]).

### B. Strongly connected nets and circuits

A net is *strongly connected* if there exists a directed path from any node in  $P \cup T$  to every other node. Let us define an *elementary circuit*  $\gamma$  (or elementary cycle) in a net as a directed path that goes from one node back to the same node without passing twice on the same node. In a strongly connected net, it is easy to show that each node belongs to an elementary circuit, and thus the name *cyclic nets* are also used to denote this class.

Given a place  $p$  of a WMG, let  $t_i$  (resp.  $t_j$ ) be its unique input (resp. output) transition as shown in Fig. 1. We denote by  $w(p) = Post(p, t_i)$  the weight of its input arc and by  $\nu(p) = Pre(p, t_j)$  the weight of its output arc. For any place  $p \in P$ , we denote by  $gcd_p$  (resp.  $lcm_p$ ) the *greatest common divisor* (resp. the *least common multiple*) of the integers  $w(p)$  and  $\nu(p)$ .

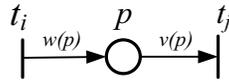


Fig. 1. A place  $p$  between two transitions  $t_i$  and  $t_j$ .

*Definition 1:* Every elementary circuit  $\gamma$  of a WMG is neutral if the following condition holds.

$$\prod_{p \in \gamma} \frac{\nu(p)}{w(p)} = 1$$

■

In other words, in a neutral circuit the product of the weights of all pre-arcs is equal to that of the weights of all post-arcs. This means that if the circuit initially contains enough tokens, it is possible to fire all transitions along the path returning to the same initial marking. It is well known that a WMG whose circuits are all neutral has a unique T-semiflow  $\alpha$  and it contains all transitions in its support [1]. In this paper, we limit our study to strongly connected WMGs in which all circuits are neutral.

A strongly connected WMG in which all circuits are neutral is *bounded*, i.e., there exists an integer  $B \in \mathbb{N}$  such that the marking of any place  $p$  is not greater than  $B$  at any reachable marking.

### C. Liveness of a TWMG

*Theorem 1:* (Theorem 4.12 of Teruel *et al.* [1]) A TWMG is live iff each elementary circuit is live. ■

In the case of a TMG, an elementary circuit is live if there exists at least one token in the circuit. The liveness decision problem of a TMG is polynomial, solved in [12], [13]. A weighted circuit of a TWMG is live if each transition can be fired infinitely. However, determining the liveness of a weighted circuit is not so easy. Up to now, no polynomial algorithm for liveness checking has been found, for example, the algorithms developed in [14] to answer this question are not polynomial. Next, we review some sufficient conditions for the liveness of weighted circuits existing in the literature. Later, these conditions will be used in the proposed optimization approach.

Teruel *et al.* [1] and Chrzastowski-Wachtel and Racunas [15] proposed a few methods to verify the liveness of weighted circuits. First they define a weighted function with respect to a marking, i.e.,

$$W(\mathbf{M}) = \mathbf{y}^T \cdot \mathbf{M} \quad (4)$$

where  $\mathbf{y}$  is a minimal P-semiflow. Furthermore, they define a marking:

$$\mathbf{M}_D = (\nu(p_1) - 1, \nu(p_2) - 1, \dots, \nu(p_n) - 1)^T \quad (5)$$

The following result provides a sufficient, albeit restrictive, condition for liveness.

*Proposition 1:* (Proposition 5.2 of Teruel *et al.* [1]) *If  $W(\mathbf{M}_0) > W(\mathbf{M}_D)$ , then the weighted circuit is live.* ■

Less restrictive conditions for liveness also exist. Let  $\mathbb{R}^+$  be a set of positive real numbers and  $\mathcal{M}(\omega) = \{\mathbf{M} | W(\mathbf{M}) = \omega, \omega \in \mathbb{R}^+\}$ . The *least live weight* is the minimal  $\omega$  such that  $\forall \mathbf{M} \in \mathcal{M}(\omega)$ ,  $\mathbf{M}$  is a live marking. In [15] the least live weight of a weighted circuit with a minimal P-semiflow  $\mathbf{y}$  was defined as

$$W_L = W(\mathbf{M}_D) - g(y_1, y_2, \dots, y_n) \quad (6)$$

where  $g$  is the Frobenius number.<sup>2</sup> Note that a Frobenius number only exists if all its arguments are greater than one and coprime. The first condition is always verified in our case since we consider minimal P-semiflows. The second condition may not always be verified: when it is, the least live weight in Eq. (6) can be computed and the following proposition holds.

*Proposition 2:* (Proposition 2.4 of Chrzastowski-Wachtel and Racunas [15]) *If  $g(y_1, y_2, \dots, y_n)$  has no non-negative integer solution and the marking  $\mathbf{M}_0$  satisfies  $W(\mathbf{M}_0) = W_L$ , then the weighted circuit is live.* ■

In the case that there exists a unitary component in a minimal P-semiflow, then a least live weight cannot be computed by Eq. (6). Let us consider the example in Fig. 2. We have  $\mathbf{y}^T = (3, 4, 3)$ ,  $\mathbf{M}_D = (3, 2, 2)^T$ ,  $\mathbf{x} = (4, 3, 3)^T$ , and  $W_L = W(\mathbf{M}_D) - g(y_1, y_2, y_3) = 23 - g(3, 4, 3) = 23 - 5 = 18$ . We conclude that any marking  $\mathbf{M}$  with weight  $W(\mathbf{M}) > 23$  or  $W(\mathbf{M}) = 18$  is a live marking.

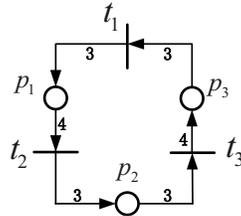


Fig. 2. A weighted circuit.

<sup>2</sup>Given positive integers  $y_1, y_2, \dots, y_n$  such that  $\gcd(y_1, y_2, \dots, y_n) = 1$ , the Frobenius number  $g(y_1, y_2, \dots, y_n)$  is the largest integer that cannot be expressed as an integer linear combination of these numbers, i.e., as a sum  $a_1 y_1 + a_2 y_2 + \dots + a_n y_n$ , where  $a_1, a_2, \dots$ , and  $a_n$  are non-negative integers.

It can be checked that every marking with a weight equal to 18 is live. For instance  $(6, 0, 0)^T$  as well as  $(0, 3, 2)^T$  is live. We use the two approaches above to select a live initial marking.

#### D. Cycle time of a TWMG

The *average cycle time*  $\chi(\mathbf{M})$  of a TWMG system  $\langle N, \mathbf{M} \rangle$  is the average time to fire once the minimal T-semiflow under the ASAP operational model (i.e., transitions are fired as soon as possible). For deterministic TWMGs, the following limit exists:

$$\lim_{\tau \rightarrow \infty} \frac{\vec{\sigma}_\tau}{\tau} = \vec{\sigma}^* < \infty$$

where the vector  $\vec{\sigma}_\tau$  represents the firing vector from time 0 to time  $\tau$  and the constant vector  $\vec{\sigma}^*$  is called the limit firing vector.  $\vec{\sigma}^*(t_i)$  represents the average number of firing  $t_i$  per time unit. The average cycle time of transition  $t_i$  of a TWMG is the average time between two consecutive firings of  $t_i$ , which is equal to

$$\frac{1}{\vec{\sigma}^*(t_i)}$$

*Definition 2:* Let  $t_i \in T$  be an arbitrary transition of a TWMG with the minimal T-semiflow  $\mathbf{x}$ . The average cycle time of the TWMG is equal to

$$\frac{x_i}{\vec{\sigma}^*(t_i)}$$

■

Note that the value of the average cycle time does not depend on the considered transition. In [16] and [17], the authors proved that the ASAP execution of a live and strongly connected TMG with integer delays is ultimately repetitive. The period of the pattern is  $\tau_{ij} = \tau_j - \tau_i$  and the number of firings of every transition within a period is  $k$  (the periodicity). In terms of TWMGs, the ASAP execution is also ultimately periodic. Fig. 3 shows the evolution of a live TWMG, where  $\mathbf{M}_0$  is the initial marking and the arrows correspond to ASAP execution steps. The number of firings of transition  $t_i$  within the steady period is  $k_i$ . This value is not identical for each transition but the proportion is equal to the minimal T-semiflow. Thus, the average cycle time of the TWMG is equal to

$$x_i \cdot \frac{\tau_{ij}}{k_i} \quad (7)$$

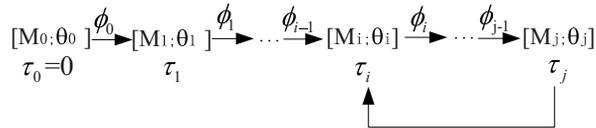


Fig. 3. Evolution of a live and strongly connected TWMG.

The average cycle time of a TWMG depends on the average cycle time of its circuits. Considering a net consisting of only one circuit, we define  $\chi_\gamma(\mathbf{M})$  as the average cycle time of circuit  $\gamma$ . Let  $\Gamma$  represent the set of elementary circuits of a cyclic TWMG and  $\chi^*(\mathbf{M}) = \max_{\gamma \in \Gamma} \chi_\gamma(\mathbf{M})$  be a critical time. Any  $\gamma \in \Gamma$  such that  $\chi_\gamma(\mathbf{M}) =$

$\chi^*(M)$  is a critical circuit that is denoted as  $\gamma^*$ . It is well known that for a TMG the average cycle time is equal to the critical time, i.e.,

$$\chi(M) = \chi^*(M)$$

This result does not apply to a TWMG as we will show in the example in Fig. 5, but it holds that the critical time is less than or equal to the average cycle time, i.e.,

$$\chi(M) \geq \chi^*(M)$$

To a certain extent, the critical circuits are those that bind the speed of the system. Up to now, no analytical method to compute the average cycle time of a TWMG has been proposed in the literature: only bounds on this value can be found in [3]. The studies in [2], [14] have proposed techniques to convert a TWMG into an equivalent TMG for which an average cycle time can be analytically computed. However, this expansion can lead to a model of significant size. A method to compute the average cycle time of a TWMG is proposed in [19] but under restrictive conditions on the initial marking. This is why in this paper we resort to a simulation study to compute the average cycle time of a TWMG using Eq. (7).

*Example:* Let us consider the evolution of the TWMG in Fig. 4(b). It starts with an initial marking  $M_0 = (10, 0)^T$  and  $\theta_0 = (+\infty, 5)$ . The evolution of the TWMG is presented as follows:

$$\begin{array}{c}
[M_0=(10,0)^T; \theta_0=(+\infty,5); \tau_0=0] \\
\quad \downarrow t_2 \\
[M_1=(6,4)^T; \theta_1=(+\infty,5); \tau_1=5] \\
\quad \downarrow t_2 \\
[M_2=(2,8)^T; \theta_2=(2,+\infty); \tau_2=10] \\
\quad \downarrow t_1 \\
[M_3=(8,2)^T; \theta_3=(+\infty,5); \tau_3=12] \\
\quad \downarrow t_2 \\
[M_4=(4,6)^T; \theta_4=(2,5); \tau_4=17] \\
\quad \downarrow t_1 \\
[M_5=(10,0)^T; \theta_5=(+\infty,3); \tau_5=19] \\
\quad \downarrow t_2 \\
[M_6=(6,4)^T; \theta_6=(+\infty,5); \tau_6=22]
\end{array}$$

Note that states  $[M_1; \theta_1]$  and  $[M_6; \theta_6]$  are the same, implying that from state  $[M_6; \theta_6]$  the system will enter a cycle which includes five states and the firing sequence is  $t_2 t_1 t_2 t_1 t_2$ . The minimal T-semiflow is  $x = (2, 3)^T$  and  $\tau_{1,6} = 17$ . The average cycle time of the TWMG is equal to 17 by solving Eq. (7).

### III. PROBLEM STATEMENT

#### A. Problem formulation

The *marking optimization problem* of a TWMG consists in finding an initial marking  $M_0$  that minimizes a weighted function of the initial marking while the average cycle time is less than or equal to a given value. In other words we aim to find a solution of the following optimization problem:

$$(P1) \begin{cases} \min f(\mathbf{M}_0) = \mathbf{y}^T \cdot \mathbf{M}_0 \\ \text{s.t. } \chi(\mathbf{M}_0) \leq b \end{cases} \quad (8)$$

where

- $\chi(\mathbf{M}_0)$  is the average cycle time of the TWMG associated with marking  $\mathbf{M}_0$ ,
- $b$  is a given positive real number that represents the upper bound of the cycle time, and
- $\mathbf{y} = \sum_{\gamma \in \Gamma} c_\gamma \cdot \mathbf{y}_\gamma$  is a P-semiflow and  $\mathbf{y}_\gamma$  is a minimal P-semiflow, i.e.,

$$y_\gamma(p_i) = y(p_i) \text{ if } p_i \in \gamma, \text{ else } y_\gamma(p_i) = 0$$

and  $c_\gamma$  represents the cost of the resources modeled by tokens in the support of  $\mathbf{y}_\gamma$ .

We choose  $\mathbf{y}$  as a P-semiflow since the value of  $\mathbf{y}^T \cdot \mathbf{M}_0$  at every reachable marking  $\mathbf{M} \in R(N, \mathbf{M}_0)$  is invariant.

*Proposition 3:* (Proposition 2 of Sauer [7]) (P1) has a solution iff  $b \geq \chi'(\mathbf{M}) = \max\{x_i \cdot \delta_i, t_i \in T\}$ , where  $\mathbf{x}$  is the minimal T-semiflow. ■

The *cycle time optimization problem* of a TWMG consists in finding an initial marking that minimizes the average cycle time, i.e., maximizes the throughput of the system, while the weighted sum of the marking (resources) is less than or equal to a given value. In other words we need to find a solution of the following optimization problem:

$$(P2) \begin{cases} \min \chi(\mathbf{M}_0) \\ \text{s.t. } \mathbf{y}^T \cdot \mathbf{M}_0 \leq s \end{cases} \quad (9)$$

where

- $\mathbf{y}$  is a P-semiflow as in (P1),
- $s$  is a given positive real number, representing the maximal available resources that can be allocated.

*Proposition 4:* (P2) has a finite solution if  $s \geq s^*$ , where

$$s^* = \min \mathbf{y}^T \cdot \mathbf{M} \\ \text{s.t. } \mathbf{y}_\gamma^T \cdot \mathbf{M} > W(\mathbf{M}_D^\gamma), \forall \gamma \in \Gamma \quad \blacksquare$$

*Proof.* Let  $\mathbf{M}$  be the initial marking of a TWMG and  $W(\mathbf{M}_D^\gamma)$  be the weighted function of marking  $\mathbf{M}_D$  restricted to circuit  $\gamma$ . If  $\forall \gamma \in \Gamma, \mathbf{y}_\gamma^T \cdot \mathbf{M} > W(\mathbf{M}_D^\gamma)$ , from Proposition 1, we conclude that each circuit of the TWMG is live. Then, the TWMG is necessarily live according to Theorem 1 and its average cycle time will be finite. □

### B. A previous approach for Problem P1

In this subsection, we briefly recall an approach dealing with the marking optimization problem of TWMGs presented by Sauer in [7].

The proposed iterative heuristic algorithm starts with an initial marking  $\mathbf{M}_0$  such that

$$\mathbf{M}_0(p) = x_{p^\bullet} \cdot \text{Pre}(p, p^\bullet), \forall p \in P.$$

Obviously, under the condition imposed by Proposition 3 this marking is feasible for Eq. (8), i.e., it satisfies  $\chi(\mathbf{M}_0) \leq b$ .

The approach requires to evaluate the average cycle time and the corresponding average marking by simulation. At each iteration step, one place is selected to remove a token from  $\mathbf{M}_0$  as long as the average cycle time is less than or equal to the upper bound  $b$ . The selected place should maximize the following criterion:

$$L(p, \infty) \cdot y_p,$$

where  $L(p, \infty)$  denotes the number of tokens in the average marking that cannot be used to enable transition  $p^\bullet$ . If  $\mathbf{M}_0(p) = 0$ , a marking reachable from  $\mathbf{M}_0$  containing at least one token is computed. The algorithm stops when there is no place that can be selected to remove tokens.

When the net size becomes larger, this approach usually requires a large number of iteration steps to remove the redundant tokens.

#### IV. MARKING OPTIMIZATION FOR TWMGS

##### A. The main idea of the heuristic solution

We propose here a fast and efficient heuristic solution based on an iterative process to solve the problem of marking optimization for TWMGs. It starts with a live marking that has a *small* weighted sum, and then we compute the average cycle time of the TWMG. If the average cycle time is greater than the upper bound of the cycle time, we add tokens to some circuits until the average cycle time is less than or equal to the upper bound of the cycle time. We select the places to which tokens should be added so as to increase the performance index  $f(\mathbf{M}_0)$  as small as possible.

1) *Useful tokens*: The initial marking  $\mathbf{M}_0(p)$  of any place  $p$  can be replaced by  $\mathbf{M}_0^*(p)$  tokens without any influence on the precedence constraints induced by  $p$  (see [20] and [21]), where

$$\mathbf{M}_0^*(p) = \left\lfloor \frac{\mathbf{M}_0(p)}{gcd_p} \right\rfloor \cdot gcd_p \quad (10)$$

As a result, we can deduce that the average cycle time at  $\mathbf{M}_0$  and  $\mathbf{M}_0^*$  are the same. However, the value of  $f(\mathbf{M}_0^*)$  is less than or equal to  $f(\mathbf{M}_0)$ . Let us consider the example in Fig. (4). The initial marking of the TWMG is  $\mathbf{M}_0 = (11, 1)^T$  and  $gcd_{p_1} = gcd_{p_2} = 2$ .

$$\begin{aligned} \mathbf{M}_0^*(p_1) &= \left\lfloor \frac{\mathbf{M}_0(p_1)}{gcd_{p_1}} \right\rfloor \cdot gcd_{p_1} = \left\lfloor \frac{11}{2} \right\rfloor \cdot 2 = 10 \\ \mathbf{M}_0^*(p_2) &= \left\lfloor \frac{\mathbf{M}_0(p_2)}{gcd_{p_2}} \right\rfloor \cdot gcd_{p_2} = \left\lfloor \frac{1}{2} \right\rfloor \cdot 1 = 0 \end{aligned}$$

Then  $\mathbf{M}_0^* = (10, 0)^T$  and we can check that  $f(\mathbf{M}_0^*) = 10 < f(\mathbf{M}_0) = 12$  and the average cycle time at  $\mathbf{M}_0$  and  $\mathbf{M}_0^*$  are identical, i.e.,  $\chi(\mathbf{M}_0) = \chi(\mathbf{M}_0^*) = 17$ .

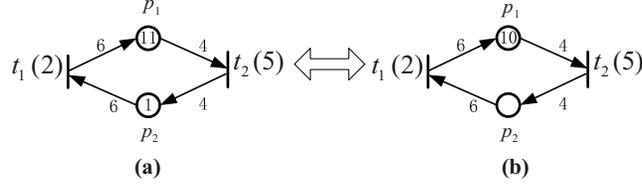


Fig. 4. Useful tokens.

2) *Selection of a proper initial marking*: For each circuit, there exist some markings that satisfy the least live weight condition. We choose the one that makes the net live while satisfies the following condition:

$$\begin{cases} \min f(\mathbf{M}) = \mathbf{y}^T \cdot \mathbf{M} \\ \text{s.t. } C(\mathbf{M}, \gamma) \quad \forall \gamma \in \Gamma \end{cases} \quad (11)$$

where

$$C(\mathbf{M}, \gamma) : \mathbf{y}_\gamma^T \cdot \mathbf{M} = W_L^\gamma \quad (12)$$

or

$$C(\mathbf{M}, \gamma) : \mathbf{y}_\gamma^T \cdot \mathbf{M} > W(\mathbf{M}_D^\gamma) \quad (13)$$

For each circuit  $\gamma$ , we consider its minimal P-semiflow. If it contains no unitary component, the least live weight  $W_L^\gamma$  of the circuit can be determined and we use Eq. (12) for  $\gamma$ , as this provides a sufficient condition for liveness with minimal cost. If the minimal P-semiflow of the circuit contains unitary components, we use Eq. (13).

We point out that it may happen that IPP (11) has no feasible solution due to the presence of the equality constraints given by Eq. (12) that may not be compatible. Should this situation occur, we use for all circuits the inequality constraints given by Eq. (13), thus ensuring that a feasible solution exists.

When there exists more than one optimal solution for the marking  $\mathbf{M}$ , we choose one. Then the initial marking  $\mathbf{M}_0$  can be computed using Eq. (10), i.e.,  $\mathbf{M}_0 = \mathbf{M}^*$ . If we start the iteration from a marking that satisfies the condition above, we can ensure that the net is live and the value of performance index  $f(\mathbf{M}_0)$  is small. If the average cycle time of  $\mathbf{M}_0$  is greater than the upper bound of the cycle time, we add tokens to the net until the requirement on the average cycle time is satisfied. Otherwise, the initial marking  $\mathbf{M}_0$  is a heuristically good solution (although possibly not optimal).

3) *Selection of the places to add tokens*: After we select the initial marking  $\mathbf{M}_0$ , we can compute the average cycle time  $\chi(\mathbf{M}_0)$  of the TWMG and  $\chi_\gamma(\mathbf{M}_0)$  for every elementary circuit. If the average cycle time satisfies the condition  $\chi(\mathbf{M}_0) \leq b$ , no more tokens should be added and the marking  $\mathbf{M}_0$  is chosen as a solution.

If the average cycle time does not satisfy the condition  $\chi(\mathbf{M}_0) \leq b$ , two situations are possible. If there exist circuits  $\gamma$ 's that have average cycle time greater than  $b$ , i.e.,  $\chi_\gamma(\mathbf{M}_0) > b$ , tokens should be added to all these circuits. The set of selected circuits is denoted as  $\Gamma_c = \{\gamma \in \Gamma | \chi_\gamma(\mathbf{M}_0) > b\}$ . However, as we discussed in II-D,

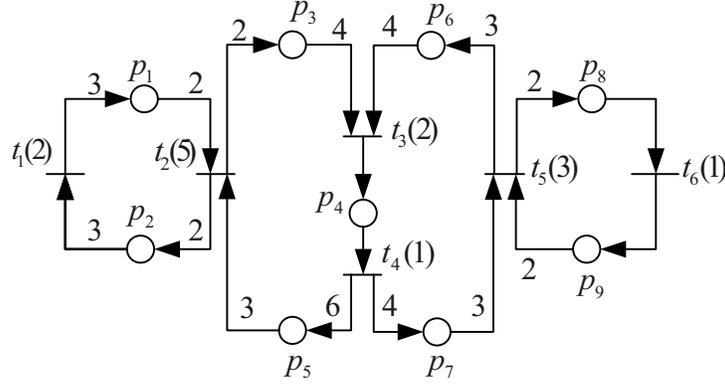


Fig. 5. The TWMG of Example 1.

it may also happen that for any circuit  $\gamma \in \Gamma$ ,  $\chi_\gamma(\mathbf{M}_0) \leq b$  holds, even if the cycle time of the net is  $\chi(\mathbf{M}_0) > b$ . In this case we choose to add tokens to all critical circuits  $\gamma^*$ , i.e., the set of circuits selected for adding tokens is  $\Gamma_c = \{\gamma \in \Gamma | \chi_\gamma(\mathbf{M}_0) = \chi^*(\mathbf{M}_0)\}$ , where  $\chi^*(\mathbf{M}_0)$  is the critical time.

For each of these circuits, we select one place  $p_r$  and add  $gcd_{p_r}$  tokens to this place. We choose the one that increases  $f(\mathbf{M}_0)$  as small as possible, i.e., the increment of the criterion value  $f(\mathbf{M}_0)$  should be the least after adding  $gcd_{p_r}$  tokens. We define an  $n$ -dimensional vector  $\mathbf{I}$  of zeros and ones.

$$\mathbf{I}^T = (I_{p_1}, I_{p_2}, \dots, I_{p_n}) \quad (14)$$

where

$$I_{p_r} = \begin{cases} 1, & \text{add } gcd_{p_r} \text{ tokens to place } p_r \\ 0, & \text{add } 0 \text{ token to place } p_r \end{cases} \quad (15)$$

In other words, we add tokens to the places with the coefficient  $I_{p_r} = 1$ . Let  $P_a$  be the set of these places and  $\mathbf{G}_d = (gcd_{p_1} \cdot y_1, gcd_{p_2} \cdot y_2, \dots, gcd_{p_n} \cdot y_n)^T$ , where  $\mathbf{y}$  is a P-semiflow of the net and  $gcd_{p_r} \cdot y_r$  represents the increment of  $f(\mathbf{M}_0)$  after adding  $gcd_{p_r}$  tokens to place  $p_r$ . We denote by  $\Delta f(\mathbf{M}_0)$  the total increment of  $f(\mathbf{M}_0)$ , where

$$\Delta f(\mathbf{M}_0) = \mathbf{I}^T \cdot \mathbf{G}_d \quad (16)$$

Then, we can select the places by solving the following problem:

$$\begin{cases} \min & \Delta f(\mathbf{M}_0) \\ \text{s.t.} & \sum_{p \in \gamma} I_p = 1, \forall \gamma \in \Gamma_c \end{cases} \quad (17)$$

The constrains in Eq. (17) will ensure that only one place should be selected for each circuit.

## B. Heuristic solution

We can summarize the proposed procedure in Algorithm 1. In step 6 of Algorithm 1, the average cycle time needs to be computed. This can be done using any of the techniques mentioned in Section II-D. In this paper, we use the Petri net tool HYPENS [24] to compute the cycle time via simulation.

### Algorithm 1: Marking Optimization

**Input:** A cyclic TWMG  $N$  with a set of elementary circuits  $\Gamma$ , an upper bound  $b$  of its average cycle time, and a

$$P\text{-semiflow } \mathbf{y} = \sum_{\gamma \in \Gamma} c_\gamma \cdot \mathbf{y}_\gamma.$$

**Output:** An initial marking  $\mathbf{M}_0$  such that the cycle time of the net satisfies  $\chi(\mathbf{M}_0) \leq b$ .

- 1: Compute the marking  $\mathbf{M}_D$  as in Eq. (5).
- 2: For every elementary circuit  $\gamma \in \Gamma$ , compute  $W(\mathbf{M}_D^\gamma) = \mathbf{y}_\gamma^T \cdot \mathbf{M}_D$ .
- 3: For every elementary circuit  $\gamma \in \Gamma$ , compute  $W_L^\gamma = \mathbf{y}_\gamma^T \cdot \mathbf{M}_D - g$  if possible.
- 4: Compute a marking  $\mathbf{M}$  that satisfies Eq. (11).
- 5: Compute an initial marking  $\mathbf{M}_0 = \mathbf{M}^*$ .
- 6: Compute the average cycle time  $\chi(\mathbf{M}_0)$  and  $\chi_\gamma(\mathbf{M}_0)$ ,  $\forall \gamma \in \Gamma$ .
- 7: If  $\chi(\mathbf{M}_0) \leq b$ , stop and  $\mathbf{M}_0$  is a solution.
- 8: while  $(\chi(\mathbf{M}_0) > b)$ 
  - {
  - If  $\exists \gamma, \chi_\gamma(\mathbf{M}_0) > b$ , tokens should be added to all these circuits in  $\Gamma_c = \{\gamma \in \Gamma | \chi_\gamma(\mathbf{M}_0) > b\}$
  - Else  $\Gamma_c = \{\gamma \in \Gamma | \chi_\gamma(\mathbf{M}_0) = \chi^*(\mathbf{M}_0)\}$ ;
  - Compute  $I$  and  $P_a$ ;
  - Add tokens to  $P_a$  and update  $\mathbf{M}_0$ ;
  - }
- 9: Output an initial marking  $\mathbf{M}_0$ .

TABLE I  
THE ITERATION PROCESS FOR EXAMPLE 1.

$\mathbf{M}_0$	$\chi_{\gamma_1}(\mathbf{M}_0)$	$\chi_{\gamma_2}(\mathbf{M}_0)$	$\chi_{\gamma_3}(\mathbf{M}_0)$	$\chi_{\gamma_4}(\mathbf{M}_0)$	$\chi(\mathbf{M}_0)$	$b$	$f(\mathbf{M}_0)$	$\Gamma_c$	$P_a$
$(4, 0, 4, 0, 0, 0, 6, 2, 0)^T$	38	39	21	20	43	30	34	$\{\gamma_1, \gamma_2\}$	$\{p_1, p_5\}$
$(5, 0, 4, 0, 3, 0, 6, 2, 0)^T$	34	30	21	20	34	30	42	$\{\gamma_1\}$	$\{p_1\}$
$(6, 0, 4, 0, 3, 0, 6, 2, 0)^T$	30	30	21	20	30	30	44		

### C. Numerical Examples

We consider Example 1 in Fig. 5. There are four weighted circuits in the TWMG:

$$\begin{cases} \gamma_1 = p_1 t_2 p_2 t_1 \\ \gamma_2 = p_3 t_3 p_4 t_4 p_5 t_2 \\ \gamma_3 = p_6 t_3 p_4 t_4 p_7 t_5 \\ \gamma_4 = p_8 t_6 p_9 t_5 \end{cases}$$

The minimal T-semiflows of  $\gamma_1$ ,  $\gamma_2$ ,  $\gamma_3$ , and  $\gamma_4$  are

$$\begin{cases} \mathbf{x}_1 = (2, 3, 0, 0, 0, 0)^T \\ \mathbf{x}_2 = (0, 2, 1, 1, 0, 0)^T \\ \mathbf{x}_3 = (0, 0, 3, 3, 4, 0)^T \\ \mathbf{x}_4 = (0, 0, 0, 0, 1, 2)^T \end{cases}$$

while the minimal P-semiflows of  $\gamma_1$ ,  $\gamma_2$ ,  $\gamma_3$ , and  $\gamma_4$  are

$$\begin{cases} \mathbf{y}_1 = (1, 1, 0, 0, 0, 0, 0, 0)^T \\ \mathbf{y}_2 = (0, 0, 3, 12, 2, 0, 0, 0)^T \\ \mathbf{y}_3 = (0, 0, 0, 4, 0, 1, 1, 0)^T \\ \mathbf{y}_4 = (0, 0, 0, 0, 0, 0, 0, 1)^T \end{cases}$$

TABLE II  
THE ITERATION PROCESS FOR EXAMPLE 2.

$\mathbf{M}_0$	$\chi_{\gamma_1}(\mathbf{M}_0)$	$\chi_{\gamma_2}(\mathbf{M}_0)$	$\chi(\mathbf{M}_0)$	$b$	$f(\mathbf{M}_0)$	$\Gamma_c$	$P_a$
$(2, 3, 0, 1, 0)^T$	29	26	30	21	20	$\{\gamma_1, \gamma_2\}$	$\{p_3\}$
$(2, 3, 2, 1, 0)^T$	25	26	26	21	24	$\{\gamma_1, \gamma_2\}$	$\{p_3\}$
$(2, 3, 4, 1, 0)^T$	21	21	21	21	28		

TABLE III  
A COMPARISON BETWEEN THE APPROACH OF SAUER AND THE APPROACH PROPOSED IN THIS PAPER (HE).

				Sauer [ave]			He [ave]			He/Sauer [ave]		
Nb. of cycles	Nb. of nets	$ P $ [ave]	$ T $ [ave]	Iteration steps	CPU time [s]	Obj. Fun.	Iteration steps	CPU time [s]	Obj. Fun.	Iteration steps	CPU time [s]	Obj. Fun.
1	10	4	4	36.5	168	29.5	3.6	18	29.7	26.4%	29.2%	101.0%
2	10	9	8	64.7	615	34.3	1.9	44	38.5	9.4%	26.8%	111.3%
4	10	15	12	279.7	3676	80.2	3.6	155	85.8	2.2%	9.4%	106.7%
6	10	22	17	387.5	8890	100.8	4	358	114.1	1.5%	6.0%	114.1%
10	10	40	31	—	—	—	4.3	753	191.5	4.3/—	753/—	191.5/—

The cost of  $\gamma_1$  and  $\gamma_3$  is twice the cost of  $\gamma_2$  and  $\gamma_4$ , i.e.,  $c_{\gamma_1} = c_{\gamma_3} = 2$  and  $c_{\gamma_2} = c_{\gamma_4} = 1$ . Therefore, the P-semiflow used in the criterion  $f(\mathbf{M}_0)$  is  $\mathbf{y} = 2\mathbf{y}_1 + \mathbf{y}_2 + 2\mathbf{y}_3 + \mathbf{y}_4 = (2, 2, 3, 20, 2, 2, 2, 1, 1)^T$ , and the minimal T-semiflow of the net is  $\mathbf{x} = (4, 6, 3, 3, 4, 8)^T$ .

$$\begin{cases} \gamma_1 : \text{since } y_1 = 1, W(\mathbf{M}_D) = 1 \times 1 + 1 \times 2 = 3 \\ \gamma_2 : W_L = W(\mathbf{M}_D) - g(y_3, y_4, y_5) = 13 - 1 = 12 \\ \gamma_3 : \text{since } y_6 = 1, W(\mathbf{M}_D) = 4 \times 0 + 1 \times 2 + 1 \times 3 = 5 \\ \gamma_4 : \text{since } y_8 = 1, W(\mathbf{M}_D) = 1 \times 0 + 1 \times 1 = 1 \end{cases}$$

We have  $gcd_{p_1} = 1, gcd_{p_2} = 1, gcd_{p_3} = 2, gcd_{p_4} = 1, gcd_{p_5} = 3, gcd_{p_6} = 1, gcd_{p_7} = 1, gcd_{p_8} = 1,$  and  $gcd_{p_9} = 1$ .

$$\mathbf{G}_d = (2, 2, 6, 20, 6, 2, 2, 1, 1)^T$$

$$\min f(\mathbf{M}) = 2\mathbf{M}(p_1) + 2\mathbf{M}(p_2) + 3\mathbf{M}(p_3) + 20\mathbf{M}(p_4) + 2\mathbf{M}(p_5) + 2\mathbf{M}(p_6) + 2\mathbf{M}(p_7) + \mathbf{M}(p_8) + \mathbf{M}(p_9)$$

$$s.t \begin{cases} \mathbf{M}(p_1) + \mathbf{M}(p_2) > 3 \\ 3\mathbf{M}(p_3) + 12\mathbf{M}(p_4) + 2\mathbf{M}(p_5) = 12 \\ 4\mathbf{M}(p_4) + \mathbf{M}(p_6) + \mathbf{M}(p_7) > 5 \\ \mathbf{M}(p_8) + \mathbf{M}(p_9) > 1 \end{cases}$$

We obtain a marking  $\mathbf{M} = (4, 0, 4, 0, 0, 0, 6, 2, 0)^T$  and the initial marking  $\mathbf{M}_0 = \mathbf{M}^* = \mathbf{M}$ . From Table I, we can find that the average cycle time of  $\gamma_1$  and  $\gamma_2$  are greater than the upper bound of the cycle time  $b$  at the initial marking  $\mathbf{M}_0$ . Then, we compute  $\mathbf{I}^T$  and  $P_a$  to add tokens.

$$\begin{aligned} \min \Delta f(\mathbf{M}_0) &= 2I_{p_1} + 2I_{p_2} + 6I_{p_3} + 20I_{p_4} + 6I_{p_5} + 2I_{p_6} + 2I_{p_7} + I_{p_8} + I_{p_9} \\ s.t \begin{cases} I_{p_1} + I_{p_2} = 1 \\ I_{p_3} + I_{p_4} + I_{p_5} = 1 \end{cases} \end{aligned}$$

We can find that  $\mathbf{I}^T = (1, 0, 0, 0, 1, 0, 0, 0, 0)$  and  $P_a = \{p_1, p_5\}$ . Then, we add one token and three tokens to places  $p_1$  and  $p_5$ , respectively. We can observe from Table I that after the first iteration step,  $\chi_{\gamma_1}(\mathbf{M}_0) > b$  holds. Then, we only need to add tokens to  $\gamma_1$  to decrease the average cycle time. The optimal marking is  $\mathbf{M} = (6, 0, 4, 0, 3, 0, 6, 2, 0)^T$  and the weight sum of tokens is  $f(\mathbf{M}) = 44$ .

Let us consider Example 2 in Fig. 6. The marking obtained by Eq. (11) is  $\mathbf{M} = (3, 3, 0, 1, 1)^T$ . We have

$$\begin{aligned} \mathbf{M}^*(p_1) &= \left\lfloor \frac{\mathbf{M}(p_1)}{gcd_{p_1}} \right\rfloor \cdot gcd_{p_1} = \left\lfloor \frac{3}{2} \right\rfloor \cdot 2 = 2 \\ \mathbf{M}^*(p_5) &= \left\lfloor \frac{\mathbf{M}(p_5)}{gcd_{p_5}} \right\rfloor \cdot gcd_{p_5} = \left\lfloor \frac{1}{2} \right\rfloor \cdot 1 = 0 \end{aligned}$$

Then the initial marking is  $\mathbf{M}_0 = \mathbf{M}^* = (2, 3, 0, 1, 0)^T$ . The iteration process is shown in Table II and the optimal marking is  $(2, 3, 4, 1, 0)^T$ .

By enumerating all the possible markings for examples in Figs. 5 and 6, we found that the solutions obtained by our proposed approach are optimal.

TABLE IV  
A COMPARISON BETWEEN THE APPROACH OF SAUER AND THE COMBINED APPROACH (HE+SAUER).

Nb. of cycles	Nb. of nets	$ P $ [ave]	$ T $ [ave]	He+Sauer [ave]			He+Sauer/Sauer [ave]			He+Sauer/He [ave]		
				Iteration steps	CPU time [s]	Obj. Fun.	Iteration steps	CPU time [s]	Obj. Fun.	Iteration steps	CPU time [s]	Obj. Fun.
1	10	4	4	4.7	23	29.5	38.9%	40.9%	100.0%	157.9%	155.6%	99.1%
2	10	9	8	4.1	63	34.3	18.6%	35.6%	100.0%	215.0%	140.7%	91.4%
4	10	15	12	6.5	193	80.2	3.9%	11.0%	100.0%	189.1%	138.9%	94.1%
6	10	22	17	8.5	472	100.8	2.9%	7.6%	100.0%	293.7%	137.2%	87.9%
10	10	40	31	11.1	973	167.3	11.1/—	973/—	167.3/—	282.0%	131.0%	87.0%

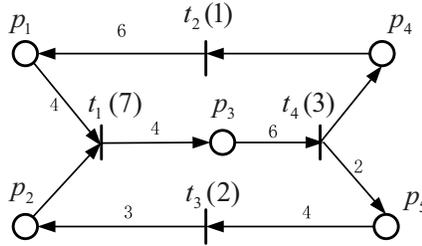


Fig. 6. The TWMG of Example 2 from [7].

#### D. Comparison with previous approaches

As we know, the previous approach dealing with the marking optimization problem of TWMG is the one presented by Sauer in [7]. We review this iterative heuristic approach in Section III-B and mention that it requires a large number of iterations since it starts from a very large feasible marking.

Adopting the heuristic solution proposed in this section, one starts with a live marking that has a *small* weighted sum. We focus our attention on the low speed circuits whose cycle times are greater than the desired value. To a certain extend, these circuits blind the speed of the system. We never add tokens to circuits whose cycle time is lower than the desired value, i.e., high speed circuits. At every iteration step, we choose one place for each selected circuit by using Eq. (17) and add tokens to it simultaneously. This procedure ensures that the cycle time of the system will decrease to the desired value rapidly. The average cycle time is computed by using simulation which stops when the system enters a cycle (see Section II-D).

In order to compare the approach of Sauer and the proposed approach, we have tested a large number of examples with different net sizes, and for each case we consider a sample of ten nets. All the samples are randomly generated under the assumption that each circuit has at least two places and at most six places. Meanwhile, for each tested example, we initialize  $b = \max\{x_i \cdot \delta_i, t_i \in T\}$ . In the proposed approach, the solution of steps 4 and 8 in Algorithm 1 is computed using Lingo, which takes a negligible time. The highest computational effort is spent in step 6 of Algorithm 1, where we need to determine the cycle time. Similarly, in Sauer's approach, the highest computational

effort is due to the repeated computation of the cycle time. Both cases use the Petri net tool HYPENS [24] to compute the cycle time via simulation. The simulation test is executed on a laptop equipped with a 1.8GHZ Core i5 Processor.

The results of a first series of tests are proposed in Table III that shows the comparison between the proposed approach (i.e., He) and that of Sauer. For all cases, we consider the average net size, the average number of iteration steps, the average CPU time, and the average value of obtained objective function. The cardinalities of  $P$  and  $T$  are approximated to the nearest integer. Note that “—” in Table III means that the computation cannot be finished within a reasonable time. As shown in Table III, we can see that the proposed method is much faster than that by Sauer [7] with the increase of the net size, while the obtained objective function is slightly worse than that of Sauer (i.e., the value of weighted sum  $Y^T \cdot \mathbf{M}_0$  is greater). The main reason that the proposed approach produces a worse result is that the initial marking computed by Eqs. (11) and (10) does not have the *least* weighted sum to ensure the liveness. Up to now, it is an interesting yet open problem to determine the least live weighted sum of a TWMG. Although we do not allocate any tokens to high speed circuits, the tokens of these circuits may still be too high.

Looking for a better and fast solution, we combine the approach proposed in this paper with that of Sauer [7], namely He+Sauer, as seen in Table IV. First, a candidate marking  $\mathbf{M}_0$  is computed by the proposed approach. Then we use the approach of Sauer to remove tokens if possible. The simulation results in Table IV present the comparison between the combined approach and the method of Sauer, and also the comparison between the combined approach and the approach proposed in this paper. Comparing the combined approach (He+Sauer) with the approach of Sauer, we always reach the same objective value while the computational costs are significantly reduced.

As one can see, the proposed method needs to find all the elementary circuits and corresponding average cycle times at the first iteration step. Then, we keep track of these slow circuits to allocate tokens. Although in practical examples, the number of circuits in a net is quite reasonable, it is well known that one may define families of nets where the number of circuits can grow exponentially as the net size increases. A case suffering from the circuit explosion is shown in Fig. 7, where  $Z_i$  ( $i = 1, \dots, n$ ) is an arbitrary integer. The set of circuits of this net is

$$\Gamma = \{p'_1 t_2 p'_2 t_3 \dots p'_n t_1 \mid (\forall i = 1, \dots, n) p'_i \in \{p_{2i-1}, p_{2i}\}\}$$

and their number is equal to  $2^n$  ( $n \geq 2$ ). The minimal P-semiflow of each circuit is the characteristic vector of the places along the circuit. Therefore, the sum of all minimal P-semiflows is  $Y = Y_1 + Y_2 + \dots + Y_{2^n} = 2^{n-1} \cdot \vec{1}_{2^n}$ , and we can choose the corresponding P-semiflow  $\mathbf{y} = \vec{1}_{2^n}$  in the criterion  $f(\mathbf{M}_0)$ . Table V shows the simulation results with different number of  $n$  and  $Z_i$  is a random integer number picked up from the interval  $[1, 6]$ . As we can see, in the case of  $n \geq 6$ , the method by Sauer will be more efficient than the proposed method.

Nevertheless, we point out this example is rather academic. In fact, an optimal solution to this problem could be found by studying the equivalent net where places  $p_2, p_4, \dots, p_{2n}$  are removed. The equivalent net contains only one circuit, hence can be efficiently studied by the proposed approach. A corresponding optimal solution for the net in Fig. 7 consists in assigning the same number of tokens to the places  $p_{2i}$  as in place  $p_{2i-1}$ .

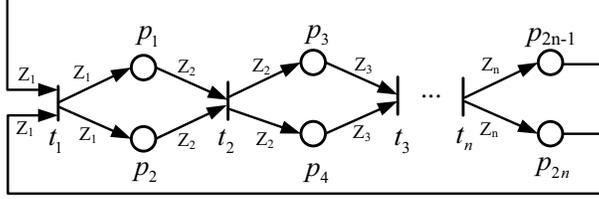


Fig. 7. Example with a large number of circuits.

TABLE V  
SIMULATION RESULTS OF THE EXAMPLE IN FIG. 7.

n	He/Sauer		
	Iteration steps	CPU time [s]	Obj. Fun.
2	3/40	29/106	22/22
4	5/66	696/1169	30/30
6	5/104	4916/4399	44/44
7	—/128	—/7320	—/50
8	—/140	—/12194	—/60
9	—/—	—/—	—/—

## V. CYCLE TIME OPTIMIZATION FOR TWMGS

### A. The main idea of the heuristic solution

In this section, we propose another heuristic solution based on an iterative process to solve the cycle time optimization problem. It starts with a feasible initial marking that makes the net live while the throughput (i.e., the inverse of the cycle time) is low, then we compute the average cycle time of the net and each circuit. It is well known that the cycle time of a TMG can be reduced if and only if tokens are added to the *critical circuit* [5]. Thus, we allocate tokens to the critical circuit at each iteration step as long as the available resources are used up or the cycle time reaches the lower bound. We add tokens to the place that satisfies a certain criterion.

1) *A criterion for selecting places:* The basic idea of the heuristic is to allocate tokens, which reduces the average cycle time  $\chi(\mathbf{M})$  as much as possible while increases the cost of resources as less as possible. After we select the

TABLE VI  
THE ITERATION PROCESS FOR EXAMPLE 1.

$\mathbf{M}_0$	$\chi_{\gamma_1}(\mathbf{M}_0)$	$\chi_{\gamma_2}(\mathbf{M}_0)$	$\chi(\mathbf{M}_0)$	$\chi'(\mathbf{M}_0)$	$\gamma^*$	$p$	$n$	$f(\mathbf{M})$
$(2, 3, 0, 1, 0)^T$	29	26	30	21	$\gamma_1$			20
$(4, 3, 0, 1, 0)^T$	25	26	26	21	$\gamma_2$	$p_1$	2	22
$(4, 4, 0, 1, 0)^T$	25	21	21	21	$\gamma_1$	$p_2$	1	26
$(6, 4, 0, 1, 0)^T$	21	21	21	21		$p_1$	2	28

initial marking, we can compute the average cycle time  $\chi(\mathbf{M})$  of the TWMG and the average cycle time  $\chi_\gamma(\mathbf{M})$  for every elementary circuit. Only when  $f(\mathbf{M}) < s$  and  $\chi(\mathbf{M}) > \chi'(\mathbf{M})$  hold, i.e., there exist available tokens to be used and the average cycle time does not reach its lower bound, we select the critical circuit  $\gamma^*$  to which tokens should be allocated. If there exists more than one critical circuit, we choose one. At each step, we select one place  $p$  and add  $n$  tokens to it. The number  $n$  is a multiple of  $gcd_p$  which represents the minimal number of tokens that we should add to decrease the average cycle time of the critical circuit. It can be computed by using simulation. We denote the decrease in the average cycle time by  $\Delta\chi_{\gamma^*}$  after allocating  $n$  tokens to place  $p$ . We have

$$\Delta\chi_{\gamma^*} = \chi_{\gamma^*}(\mathbf{M}') - \chi_{\gamma^*}(\mathbf{M}) \quad (18)$$

where  $\mathbf{M}'$  is the marking such that  $\mathbf{M}'(p) = \mathbf{M}(p) + n$  and  $\mathbf{M}'(p') = \mathbf{M}(p')$  if  $p' \neq p$ . Let  $g_p$  be the gain in criterion value, i.e., the resources that we add, where

$$g_p = y_p \cdot n \quad (19)$$

We introduce a criterion  $\Delta_p$  in which  $p$  takes into account both the decreasing of the average cycle time and the gain in criterion value, i.e.,

$$\Delta_p = \frac{g_p}{\Delta\chi_{\gamma^*}} \quad (20)$$

Tokens will be allocated to the place such that

$$\Delta_{p^*} = \min \Delta_p \quad (21)$$

Note that, the computation of  $\Delta_p$  is simple: the amount of computation is proportional to the number of places which belong to the critical circuit. At each iteration step, if there is more than one place with minimal value of  $\Delta_p$ , we keep the optimal allocations that have the minimal average cycle time to next iteration step. Before adding  $n$  tokens to the selected place  $p$ , we will check if  $f(\mathbf{M}') > s$  is true. In case that  $f(\mathbf{M}') > s$  holds, another place  $p' \in \gamma^* \cap P \setminus \{p\}$  should be selected instead of place  $p$ . We stop the allocation of tokens to the system until all available resources are used up or the average time reaches the lower bound.

TABLE VII  
CRITERION VALUES FOR ALL ITERATION STEPS FOR EXAMPLE 1.

$\mathbf{M} = (2, 3, 0, 1, 0)^T$	$p_1$	$p_3$	$p_4$	$\mathbf{M} = (4, 3, 0, 1, 0)^T$	$p_2$	$p_3$	$p_5$	$\mathbf{M} = (4, 4, 0, 1, 0)^T$	$p_1$	$p_3$	$p_5$
$n$	2	2	1		1	4	2		2	2	1
$\Delta\chi_{\gamma^*}$	4	4	7		5	5	5		4	4	4
$g_p$	2	4	6		4	8	6		2	4	3
$\Delta_p$	0.5	1	0.85		0.8	1.6	1.2		0.5	1	0.75

2) *Heuristic solution*: We can summarize the proposed procedure in Algorithm 2.

**Algorithm 2**: Cycle time optimization

**Input**: A cyclic TWMG  $N$  with a set of elementary circuits  $\Gamma$ , an upper bound  $s$  of its weighted sum of tokens, and a P-semiflow  $\mathbf{y} = \sum_{\gamma \in \Gamma} c_\gamma \cdot \mathbf{y}_\gamma$ .

**Output**: An initial marking  $\mathbf{M}_0$  such that the weighted sum of tokens satisfies  $\mathbf{y}^T \cdot \mathbf{M}_0 \leq s$ .

- 1: Compute the marking  $\mathbf{M}_D$  as in Eq. (5).
- 2: For every elementary circuit  $\gamma \in \Gamma$ , compute  $W(\mathbf{M}_D^\gamma) = \mathbf{y}_\gamma^T \cdot \mathbf{M}_D$ .
- 3: For every elementary circuit  $\gamma \in \Gamma$ , compute  $W_L^\gamma = \mathbf{y}_\gamma^T \cdot \mathbf{M}_D - g$  if possible.
- 4: Compute a marking  $\mathbf{M}$  that satisfies Eq. (11).
- 5: Compute an initial marking  $\mathbf{M}_0 = \mathbf{M}^*$ .
- 6: Compute the average cycle time  $\chi(\mathbf{M}_0)$  and  $\chi_\gamma(\mathbf{M}_0)$ ,  $\forall \gamma \in \Gamma$ .
- 7: If  $\chi(\mathbf{M}_0) \leq \chi'(\mathbf{M})$ , stop and  $\mathbf{M}_0$  is a solution.
- 8: while ( $f(\mathbf{M}_0) < s$  and  $\chi(\mathbf{M}_0) > \chi'(\mathbf{M})$ )
  - {
  - Select a critical circuit  $\gamma^*$ ;
  - Compute  $\Delta_p$ ,  $\forall p \in \gamma^*$ ;
  - Add tokens to the selected place  $p$ ,  $\mathbf{M}_0(p) := \mathbf{M}_0(p) + n$ ;
  - }
- 9: Output an initial marking  $\mathbf{M}_0$ .

### B. Numerical Examples

In this section, we apply Algorithm 2 to the cycle time optimization of a TWMG and a flexible manufacturing system (FMS).

1) *First example*: Let us consider the net in Fig. 6. Let  $s = 30$  be the maximum number of available resources to be allocated. In Table VI, all optimal allocations are given.

We can find that  $\gamma_1$  is a critical circuit at the initial marking  $\mathbf{M}_0$ . Accordingly, we compute  $g_p$  and  $\Delta_{\chi_{\gamma^*}}$  to select the place to which tokens should be added. Table VII shows the criterion values for all iteration steps. After three steps, we stop the iteration since the average cycle time converges to the lower bound. The optimal solution is  $\mathbf{M}_0 = (6, 4, 0, 1, 0)^T$ .

2) *Optimization of a flexible manufacturing system*: In this subsection, we deal with the example in Fig. 8 from [22]. An FMS composed of three machines  $\mathcal{U}_1, \mathcal{U}_2$  and  $\mathcal{U}_3$  is considered. It can manufacture two products, denoted by  $\mathcal{R}_1$  and  $\mathcal{R}_2$ . The production mix is 60% and 40% for  $\mathcal{R}_1$  and  $\mathcal{R}_2$ , respectively. The production processes of these products are  $\mathcal{R}_1 : (\mathcal{U}_1, \mathcal{U}_2, \mathcal{U}_3)$  and  $\mathcal{R}_2 : (\mathcal{U}_2, \mathcal{U}_1)$ .

In this model, there are three types of elementary circuits:

- *Process circuits*: Model the manufacturing process. The tokens belonging to these circuits represent transportation resources.

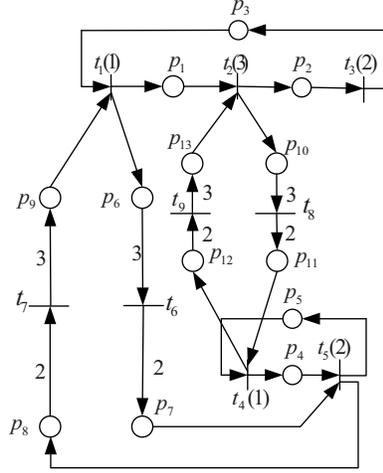


Fig. 8. The TWMG model of a flexible manufacturing system.

- *Command circuits*: Model the control of the system. One command circuit is associated with each machine.
- *Mixed circuits*: These circuits are partially composed of parts of the command circuits and parts of the process circuits.

There are six circuits in the FMS:

$$\left\{ \begin{array}{l} \gamma_1 = p_1 t_2 p_2 t_3 p_3 t_1 \\ \gamma_2 = p_4 t_5 p_5 t_4 \\ \gamma_3 = p_{10} t_8 p_{11} t_4 p_{12} t_9 p_{13} t_2 \\ \gamma_4 = p_6 t_6 p_7 t_5 p_8 t_7 p_9 t_1 \\ \gamma_5 = p_2 t_3 p_3 t_1 p_6 t_6 p_7 t_5 p_5 t_4 p_{12} t_9 p_{13} t_2 \\ \gamma_6 = p_{10} t_8 p_{11} t_4 p_4 t_5 p_8 t_7 p_9 t_1 p_1 t_2 \end{array} \right.$$

where  $\gamma_1$  and  $\gamma_2$  are process circuits,  $\gamma_3$  and  $\gamma_4$  are command circuits, and  $\gamma_5$  and  $\gamma_6$  are mixed circuits. The command circuits that model the control of the system must prevent two transitions corresponding to the same machine from being fired simultaneously. Then, they need to satisfy the condition:

$$M(p_{10}) + M(p_{13}) = 3 \quad \text{and} \quad M(p_{11}) + M(p_{12}) = 0 \quad (22)$$

or

$$M(p_{11}) + M(p_{12}) = 2 \quad \text{and} \quad M(p_{10}) + M(p_{13}) = 0 \quad (23)$$

For the command circuit  $\gamma_3$  in Fig. 8, the conservative component is:

$$2M(p_{10}) + 3M(p_{11}) + 3M(p_{12}) + 2M(p_{13}) = 6 \quad (24)$$

and this command circuit cannot be allocated tokens any more.

TABLE VIII  
THE ITERATION PROCESS FOR THE FMS.

$M_0$	$\chi_{\gamma_1}$	$\chi_{\gamma_2}$	$\chi_{\gamma_3}$	$\chi_{\gamma_4}$	$\chi_{\gamma_5}$	$\chi_{\gamma_6}$	$\chi$	$\chi'$	$\gamma^*$	$p$	$n$	$f(M_0)$
$(1,0,0,1,0,0,1,1,1,0,0,2,0)^T$	18	6	11	7	15	9	21	11	$\gamma_1$			35
$(1,1,0,1,0,0,1,1,1,0,0,2,0)^T$	9	6	11	7	12	9	13	11	$\gamma_5$	$p_2$	1	38
$(1,0,1,1,0,0,1,1,1,0,0,2,0)^T$	9	6	11	7	12	9	13	11	$\gamma_5$	$p_3$	1	38
$(1,1,0,1,1,0,1,1,1,0,0,2,0)^T$	9	4	11	7	9	9	11	11		$p_5$	1	42
$(1,0,1,1,1,0,1,1,1,0,0,2,0)^T$	9	4	11	7	9	9	11	11		$p_5$	1	42

The minimal T-semiflows of  $\gamma_1, \gamma_2, \gamma_3, \gamma_4, \gamma_5,$  and  $\gamma_6$  are

$$\left\{ \begin{array}{l} \mathbf{x}_1 = (1, 1, 1, 0, 0, 0, 0, 0, 0)^T \\ \mathbf{x}_2 = (0, 0, 0, 1, 1, 0, 0, 0, 0)^T \\ \mathbf{x}_3 = (0, 3, 0, 2, 0, 0, 0, 1, 1)^T \\ \mathbf{x}_4 = (3, 0, 0, 0, 2, 1, 1, 0, 0)^T \\ \mathbf{x}_5 = (3, 3, 3, 2, 2, 1, 0, 0, 1)^T \\ \mathbf{x}_6 = (3, 3, 0, 2, 2, 0, 1, 1, 0)^T \end{array} \right.$$

and the minimal P-semiflows of  $\gamma_1, \gamma_2, \gamma_3, \gamma_4, \gamma_5$  and  $\gamma_6$  are

$$\left\{ \begin{array}{l} \mathbf{y}_1 = (1, 1, 1, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0)^T \\ \mathbf{y}_2 = (0, 0, 0, 1, 1, 0, 0, 0, 0, 0, 0, 0, 0)^T \\ \mathbf{y}_3 = (0, 0, 0, 0, 0, 0, 0, 0, 0, 2, 3, 3, 2)^T \\ \mathbf{y}_4 = (0, 0, 0, 0, 0, 2, 3, 3, 2, 0, 0, 0, 0)^T \\ \mathbf{y}_5 = (0, 2, 2, 0, 3, 2, 3, 0, 0, 0, 0, 3, 2)^T \\ \mathbf{y}_6 = (2, 0, 0, 3, 0, 0, 0, 3, 2, 2, 3, 0, 0)^T \end{array} \right.$$

We assume that  $c_\gamma = 1, \forall \gamma \in \Gamma$ . Therefore, the P-semiflow used in the performance index is  $\mathbf{y} = \mathbf{y}_1 + \mathbf{y}_2 + \mathbf{y}_3 + \mathbf{y}_4 + \mathbf{y}_5 + \mathbf{y}_6 = (3, 3, 3, 4, 4, 4, 6, 6, 4, 4, 6, 6, 4)^T$ , and the minimal T-semiflow of the net is  $\mathbf{x} = (3, 3, 3, 2, 2, 1, 1, 1, 1)^T$ .

Note that, both process circuits  $\gamma_1$  and  $\gamma_2$  are timed marked graphs. We only need put one token in each circuit initially. We have

$$\left\{ \begin{array}{l} \gamma_4 : W_L = 6, W(M_D) = 7 \\ \gamma_5 : W_L = 6, W(M_D) = 7 \\ \gamma_6 : W_L = 6, W(M_D) = 7 \end{array} \right.$$

TABLE IX  
SOLUTIONS OBTAINED BY ALGORITHM 2.

Nb. of cycles	Nb. of nets	$ P $ [ave]	$ T $ [ave]	Ave. iteration steps	Ave. CPU time [s]	Ave. obtained cycle time
1	10	4	4	3.6	49	21.4
2	10	9	8	3.4	114	38.8
4	10	15	12	4.3	233	85.2
6	10	22	17	6.1	615	136.1
10	10	40	31	6.6	1121	324.3

and  $gcd_{p_1} = 1$ ,  $gcd_{p_2} = 1$ ,  $gcd_{p_3} = 1$ ,  $gcd_{p_4} = 1$ ,  $gcd_{p_5} = 1$ ,  $gcd_{p_6} = 1$ ,  $gcd_{p_7} = 1$ ,  $gcd_{p_8} = 1$ ,  $gcd_{p_9} = 1$ ,  $gcd_{p_{10}} = 1$ ,  $gcd_{p_{11}} = 1$ ,  $gcd_{p_{12}} = 1$ , and  $gcd_{p_{13}} = 1$ .

$$\min f(\mathbf{M}) = 3\mathbf{M}(p_1) + 3\mathbf{M}(p_2) + 3\mathbf{M}(p_3) + 4\mathbf{M}(p_4) + 4\mathbf{M}(p_5) + 4\mathbf{M}(p_6) + 6\mathbf{M}(p_7) + 6\mathbf{M}(p_8) + 4\mathbf{M}(p_9) + 4\mathbf{M}(p_{10}) + 6\mathbf{M}(p_{11}) + 6\mathbf{M}(p_{12}) + 4\mathbf{M}(p_{13})$$

$$s.t \left\{ \begin{array}{l} \mathbf{M}(p_1) + \mathbf{M}(p_2) + \mathbf{M}(p_3) \geq 1 \\ \mathbf{M}(p_4) + \mathbf{M}(p_5) \geq 1 \\ 2\mathbf{M}(p_{10}) + 3\mathbf{M}(p_{11}) + 3\mathbf{M}(p_{12}) + 2\mathbf{M}(p_{13}) = 6 \\ 2\mathbf{M}(p_6) + 3\mathbf{M}(p_7) + 3\mathbf{M}(p_8) + 2\mathbf{M}(p_9) > 7 \\ 2\mathbf{M}(p_2) + 2\mathbf{M}(p_3) + 3\mathbf{M}(p_5) + 2\mathbf{M}(p_6) + 3\mathbf{M}(p_7) \\ + 3\mathbf{M}(p_{12}) + 2\mathbf{M}(p_{13}) > 7 \\ 2\mathbf{M}(p_1) + 3\mathbf{M}(p_4) + 3\mathbf{M}(p_8) + 2\mathbf{M}(p_9) + 2\mathbf{M}(p_{10}) \\ + 3\mathbf{M}(p_{11}) > 7 \end{array} \right.$$

Note that in this example, if we use  $W_L$  for  $\gamma_4$ ,  $\gamma_5$  and  $\gamma_6$  to compute an initial marking, there is no feasible solution since the constraints are overly strict. We find the initial marking

$$\mathbf{M}_0 = \mathbf{M}^* = (1, 0, 0, 1, 0, 0, 1, 1, 1, 0, 0, 2, 0)^T$$

Let  $s=45$  be the maximum number of available resources that can be allocated. Table VIII shows the iteration process. Both  $(1, 1, 0, 1, 1, 0, 1, 1, 1, 0, 0, 2, 0)^T$  and  $(1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 0, 2, 0)^T$  are optimal solutions.

We have tested some examples with different net sizes for Algorithm 2 and the simulation results are summarized in Table IX. Note that according to Proposition 4, the number of available resources  $s$  should be greater than  $s^*$  for each tested net.

## VI. CONCLUSION

This paper deals with deterministic timed weighted marked graphs. We address the problem of marking optimization and cycle time optimization of a TWMG. The marking optimization problem consists in finding an initial marking to minimize the weighted sum of tokens in places while the average cycle time is less than or equal to a given value. The cycle time optimization problem consists in finding an initial marking to minimize the average cycle time while the weighted sum of tokens in places is less than or equal to a given value. Two heuristic algorithms are proposed to solve these problems.

Numerical simulation studies show that the proposed method in Section IV-B requires less iteration steps and thus is much more efficient than the approach in [7]. In some special cases the objective function obtained may be worse than the one found by Sauer. However, we show that by combining the two approaches, we always reach the same objective function by Sauer [7] with a significant reduction of computational costs. Future work includes finding an analytical solution to compute the average cycle time and proposing optimal solutions to the two optimization problems.

## REFERENCES

- [1] E. Teruel, P. Chrzastowski-Wachtel, J.M. Colom, and M. Silva. "On weighted T-Systems," *Appl. Theory Petri Nets*, vol. 616, pp. 348–367, 1992.
- [2] M. Nakamura and M. Silva. "Cycle time computation in deterministically timed weighted marked graphs," in *Proc. 7th IEEE Int. Conf. Emerg. Technol. Factory Autom.*, vol. 2, pp. 1037–1046, 1999.
- [3] J. Campos, G. Chiola, and M. Silva. "Ergodicity and throughput bounds of Petri nets with unique consistent firing count vector," *IEEE Trans. Software Eng.*, vol. 17, no. 2, pp. 117–125, 1991.
- [4] A. Benabid-Najjar, C. Hanen, O. Marchetti, and A. Munier. "Periodic schedules for bounded timed weighted event graphs," *IEEE Trans. Automat. Control*, vol. 57, no. 5, pp. 1222–1232, 2012.
- [5] A. Giua, A. Piccaluga, and C. Seatzu. "Firing rate optimization of cyclic timed event graphs," *Automatica*, vol. 38, no. 1, pp. 91–103, 2002.
- [6] S. Lafitit, J.M. Proth, and X. Xie. "Optimisation of invariant criteria for event graphs," *IEEE Trans. Automat. Control*, vol. 37, no. 5, pp. 547–555, 1992.
- [7] N. Sauer. "Marking optimization of weighted marked graphs," *Discrete Event Dyn. Syst.*, vol. 13, no. 3, pp. 245–262, 2003.
- [8] L. Toursi and N. Sauer. "Branch and bound approach for marking optimization problem of weighted marked graphs," in *Proc. IEEE Int. Conf. Syst., Man, Cybern.*, vol. 2, pp. 1777–1782, 2004.
- [9] Z. He, Z.W. Li, and A. Giua. "Marking optimization of deterministic timed weighted marked graphs," in *Proc. 10th IEEE Int. Conf. Autom. Sci. Eng.*, Taipei, Taiwan, August 2014.
- [10] J. Campos, G. Chiola, J.M. Colom, and M. Silva. "Properties and performance bounds for timed marked graphs," *IEEE Trans. Fundam. Theory Appl.*, vol. 39, no. 5, pp. 386–401, 1992.
- [11] T. Murata. "Petri nets: Properties, analysis and applications," in *Proc. IEEE*, vol. 77, no. 4, pp. 541–580, 1989.
- [12] C.V. Ramamoorthy and G.S. Ho. "Performance evaluation of asynchronous concurrent systems using Petri nets," *IEEE Trans. Software Eng.*, vol. 6, no. 5, pp. 440–449, 1980.
- [13] C. Ramchandani. "Analysis of asynchronous concurrent systems by timed Petri nets," Ph.D. dissertation, MIT, Cambridge, MA, 1973.
- [14] A. Munier. "Régime asymptotique optimal d'un graphe d'événements temporisé généralisé: Application à un problème d'assemblage," *APII*, vol. 27, pp. 487–513, 1993.
- [15] P. Chrzastowski-Wachtel and M. Raczunas. "Liveness of weighted circuits and the diophantine problem of frobenius," *Fund. Comput. Theory*, vol. 710, pp. 171–180, 1993.

- [16] A.J. Carlier and P. Chtienne. “Problème d’ordonnement: Modélisation, complexité, algorithmes,” Masson, Paris, 1988.
- [17] J.V. Millo and R. De Simone. “Periodic scheduling of marked graphs using balanced binary words,” *Theor. Comput. Sci.*, vol. 458, pp. 113–130, 2012.
- [18] F. Baccelli, G. Cohen, G.J. Olsder, and J.P. Quadrat. “Synchronization and linearity: An algebra for discrete event systems,” *John Wiley & Sons*, 1992.
- [19] D.Y. Chao, M.C. Zhou, and D.T. Wang. “Multiple weighted marked graphs,” in *Proc. 12th IFAC Triennial World Congress*, vol. 4, pp. 259–263, 1993.
- [20] O. Marchetti and A. Munier. “A sufficient condition for the liveness of weighted event graphs,” *Eur. J. Oper. Res.*, vol. 197, no. 2, pp. 532–540, 2009.
- [21] O. Marchetti and A. Munier. “Complexity results for weighted timed event graphs,” *Discrete Optim.*, vol. 7, no. 3, pp. 166–180, 2010.
- [22] J.M. Proth, N. Sauer, and X. Xie. “Optimization of the number of transportation devices in flexible manufacturing systems using event graphs,” *IEEE Trans. Ind. Electron.*, vol. 44, no. 3, pp. 298–306, 1997.
- [23] A.H. Ghamarian, M.C.W. Geilen, S. Stuijk, T. Basten, A.J.M. Moonen, M.J.G. Bekooij, B.D. Theelen, and M.R. Mousavi. “Throughput analysis of synchronous data flow graphs,” in *Proc. 6th IEEE Int. Conf. Appl. Concurrency Syst. Design*, pp. 25–36, 2006.
- [24] HYPENS: <http://www.diee.unica.it/automatica/hypens/>



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